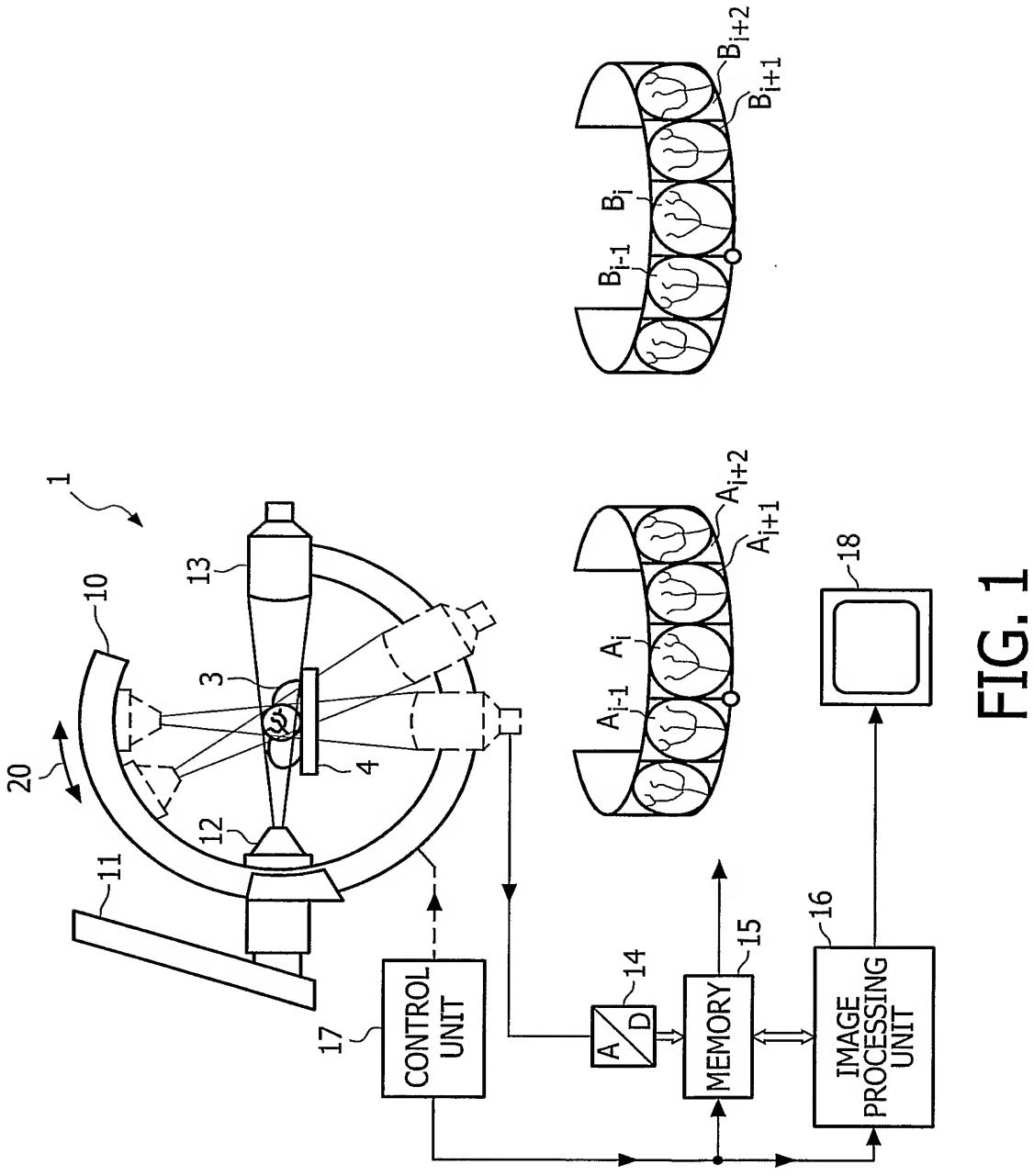
1
10
13
3
4
12
11
20
17
CONTROL UNIT
14
A/D
15
MEMORY
16
IMAGE PROCESSING UNIT
18
A_{i-1}
A_i
A_{i+1}
A_{i+2}
B_{i-1}
B_i
B_{i+1}
B_{i+2}

FIG. 1